

FIG. 1

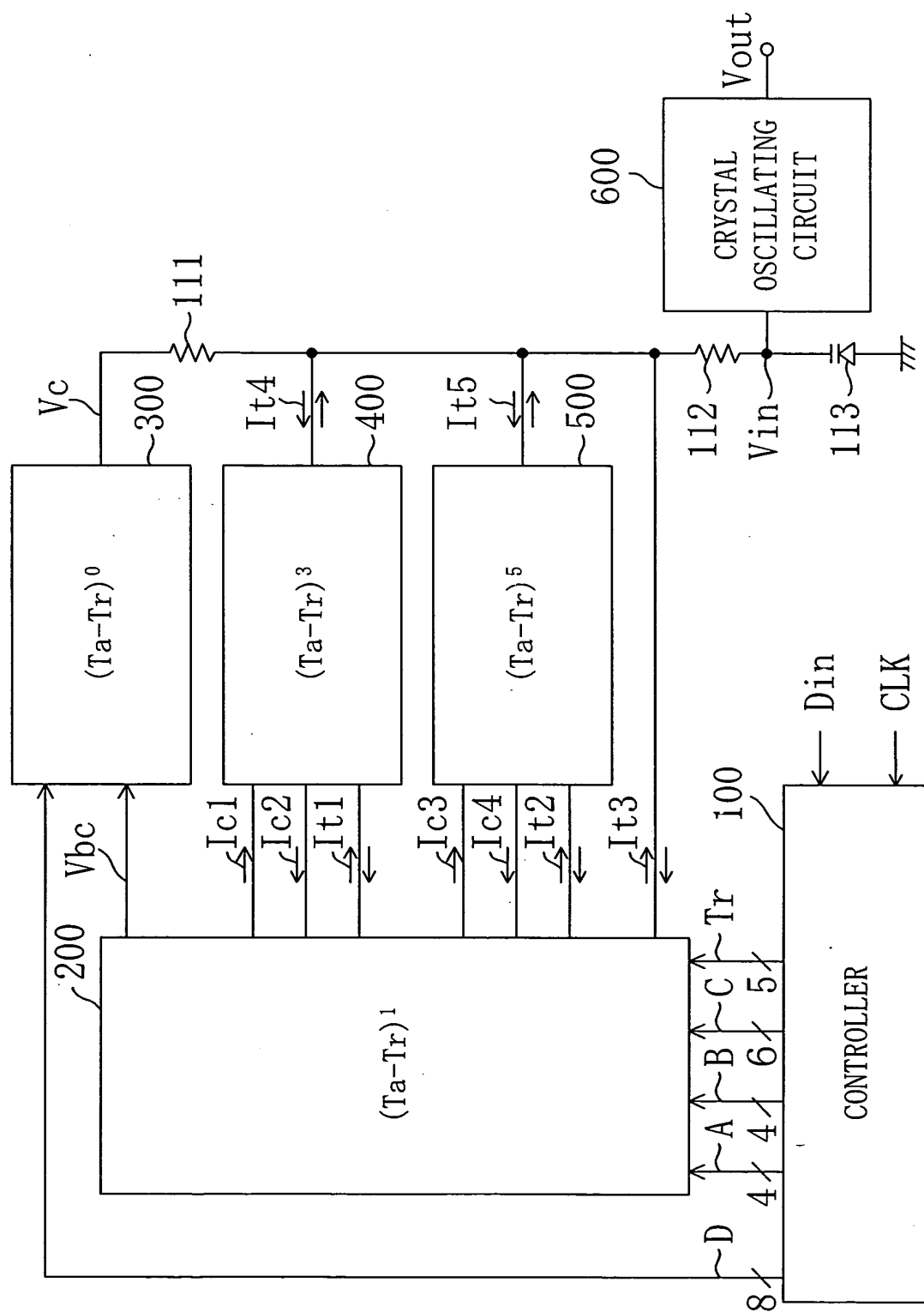


FIG. 2

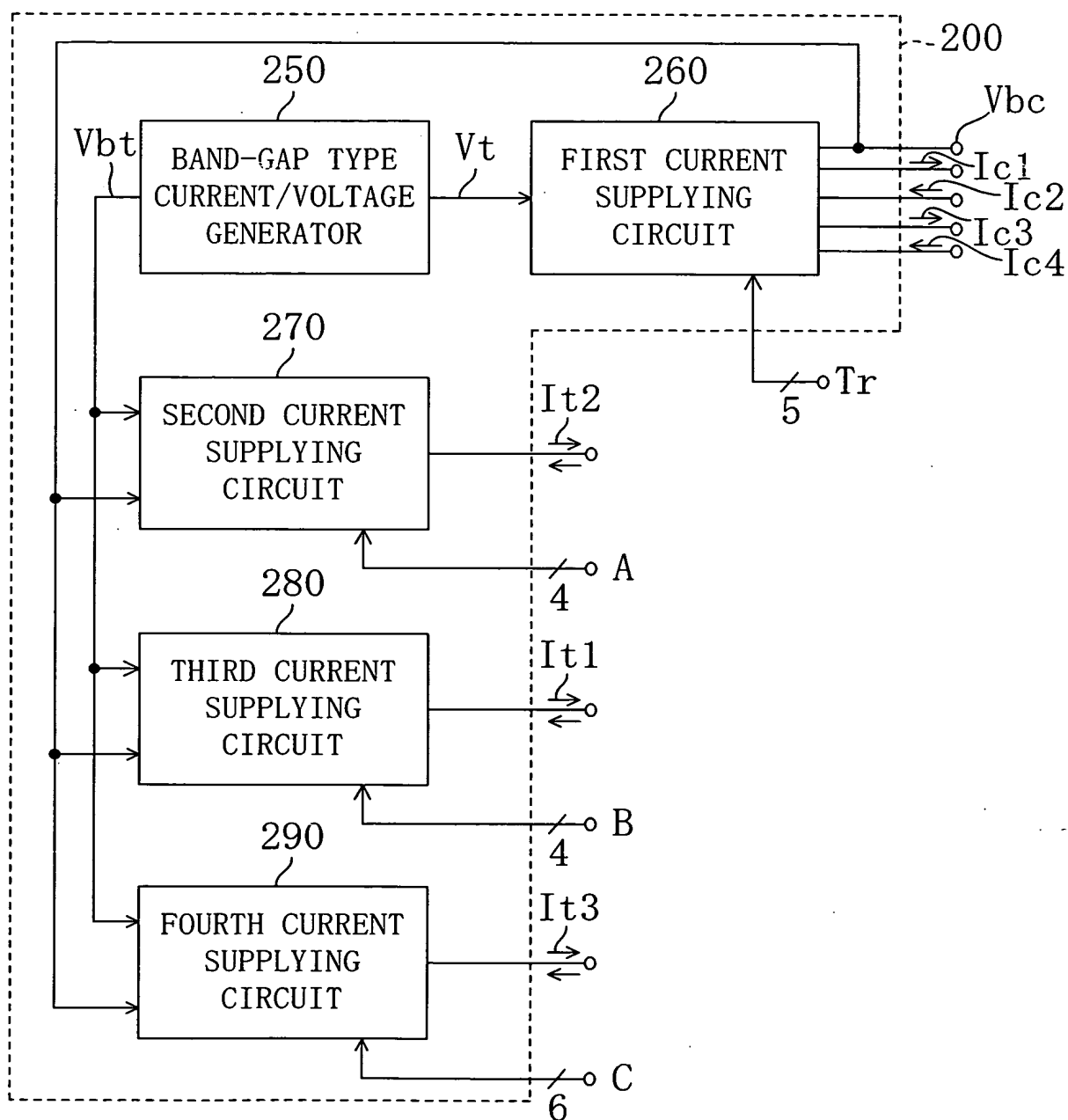


FIG. 3

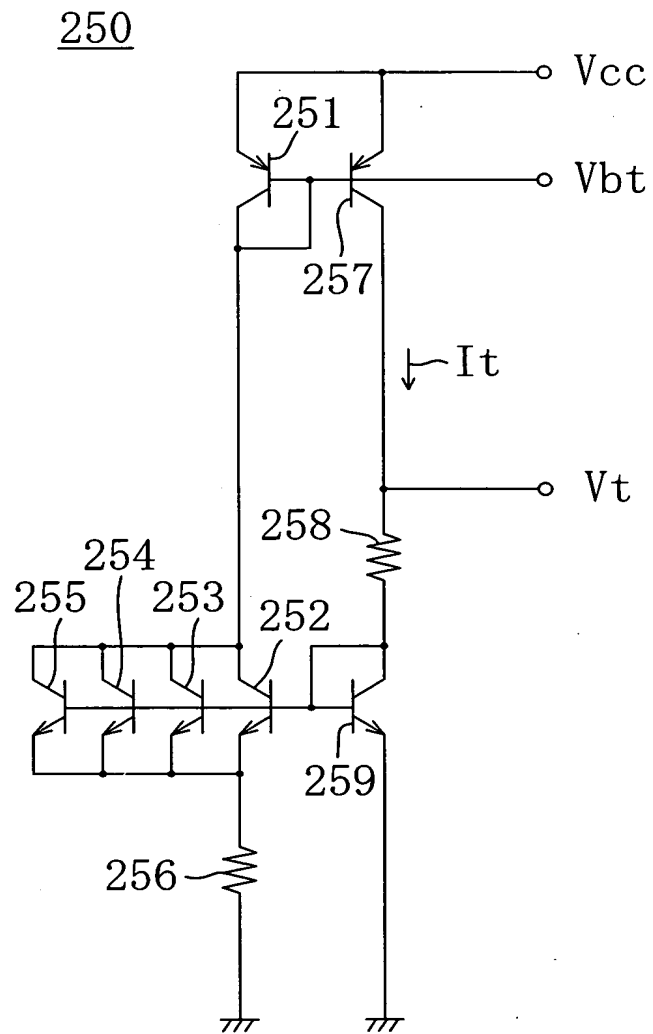


FIG. 4

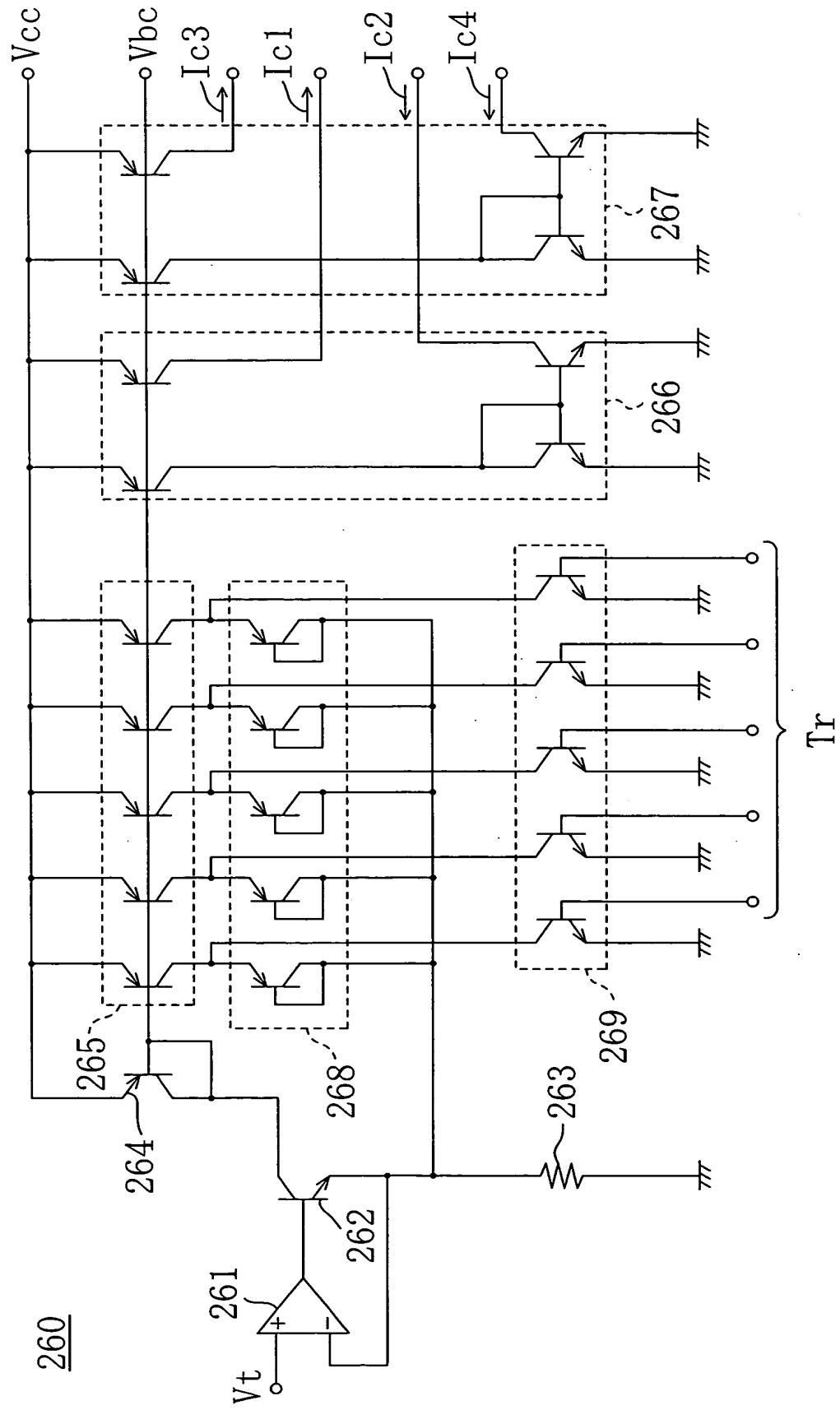


FIG. 5

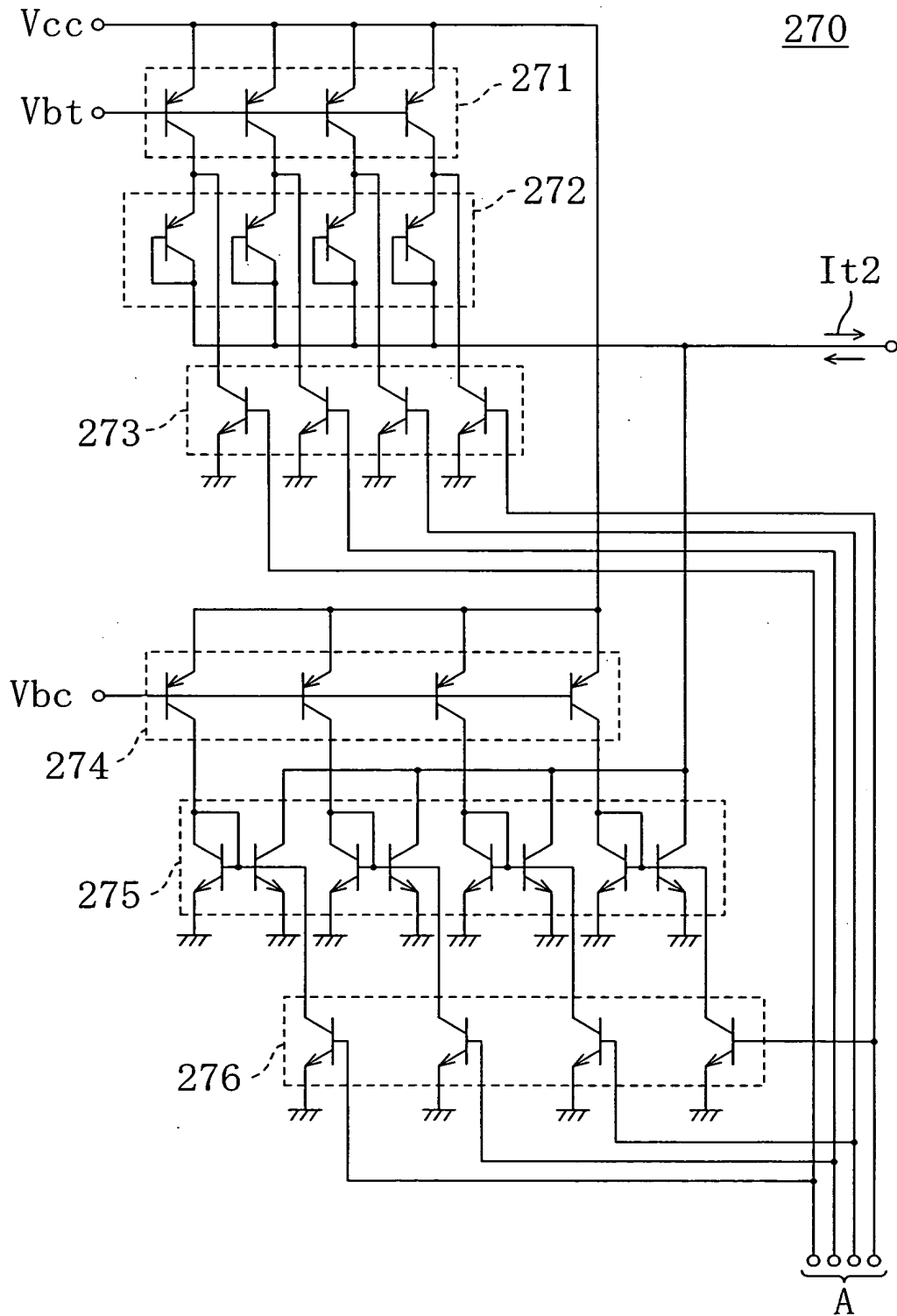
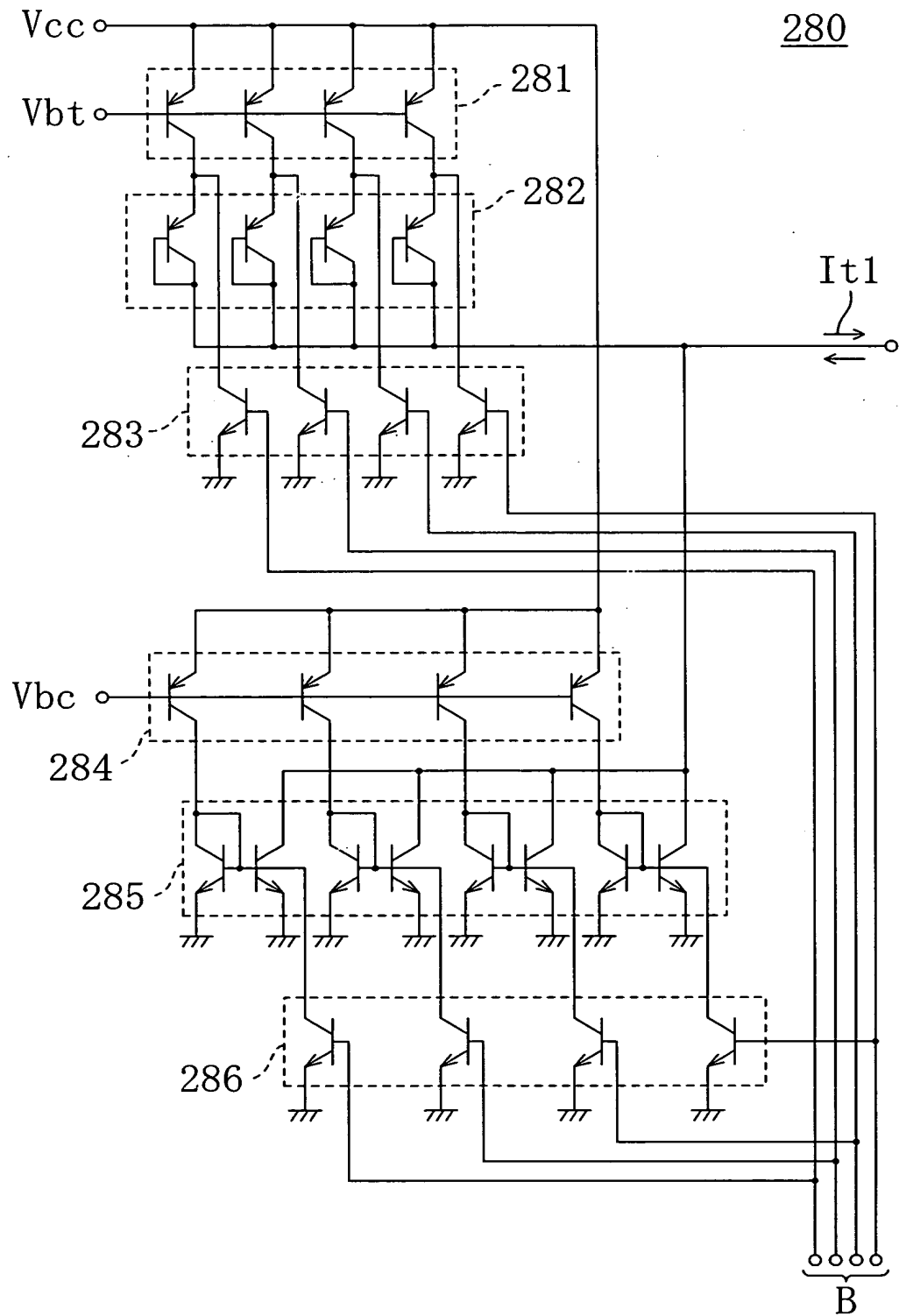


FIG. 6



The diagram illustrates a 10-bit DAC circuit using a 2-5-2 architecture. It consists of two 5-bit DAC blocks. The top block (291-293) has inputs V_{cc} and V_{bt} , and its output I_{t3} is connected to a common output bus C . The bottom block (294-296) has input V_{bc} and its output is also connected to the common output bus C . The circuit uses a combination of PMOS and NMOS transistors, with dashed boxes indicating the internal structure of the DAC blocks.

FIG. 8

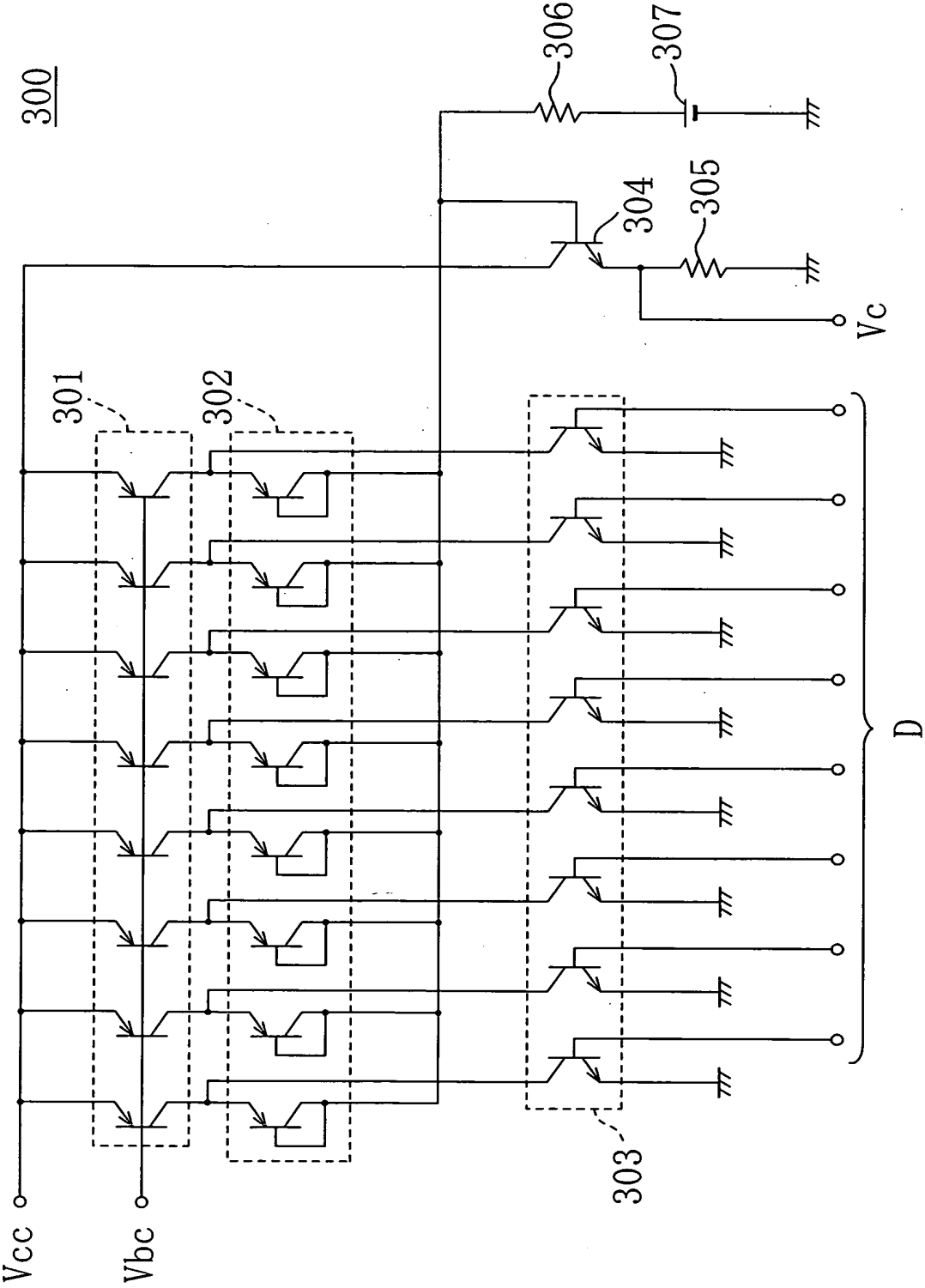


FIG. 9

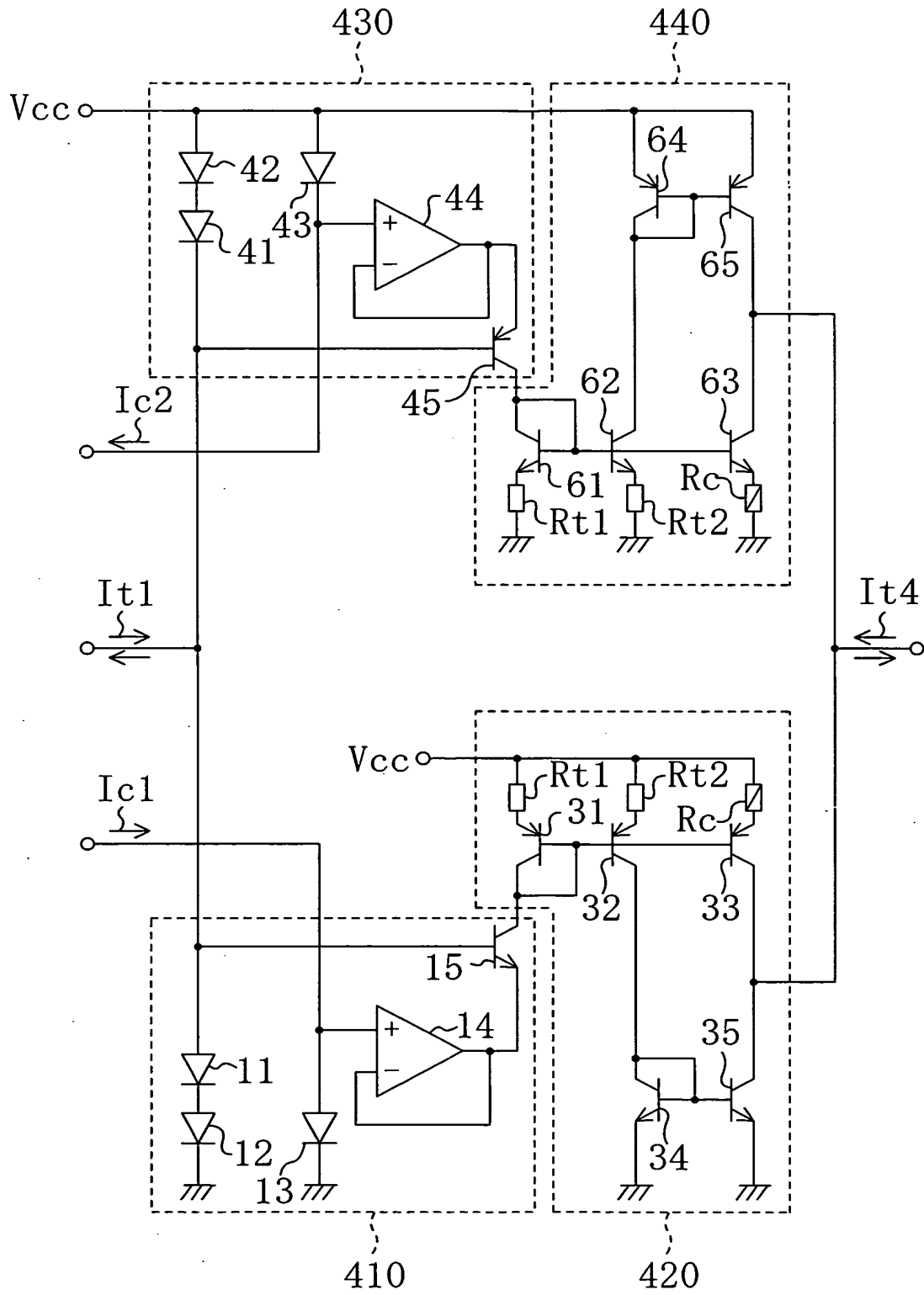
400

FIG. 10A

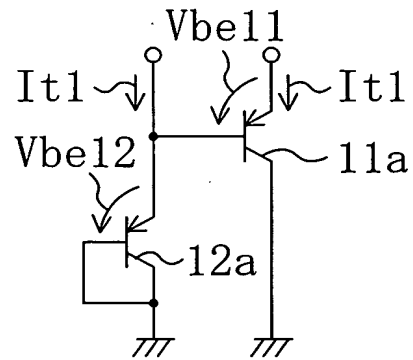


FIG. 10B

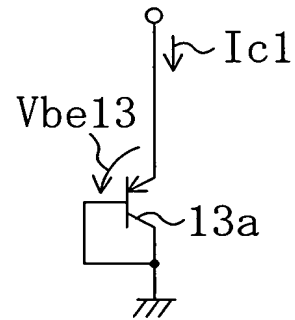


FIG. 11A

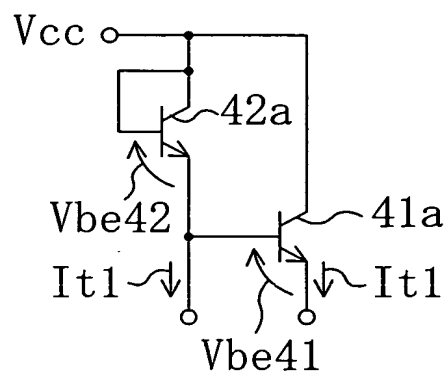


FIG. 11B

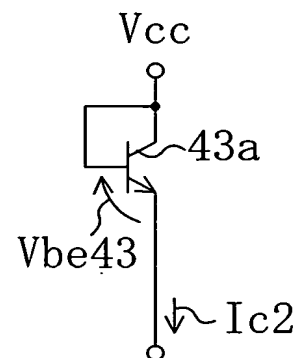


FIG. 12

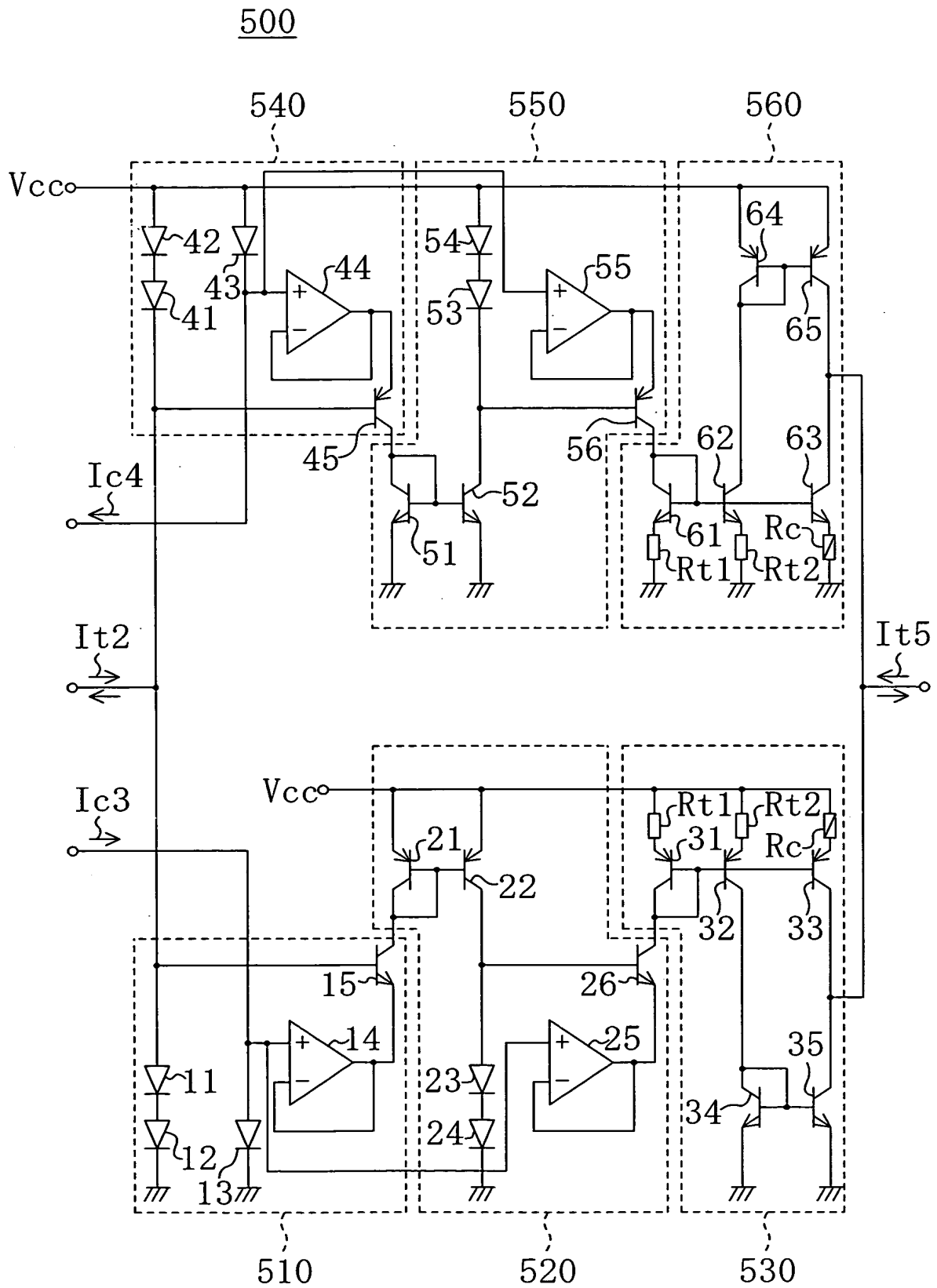


FIG. 13

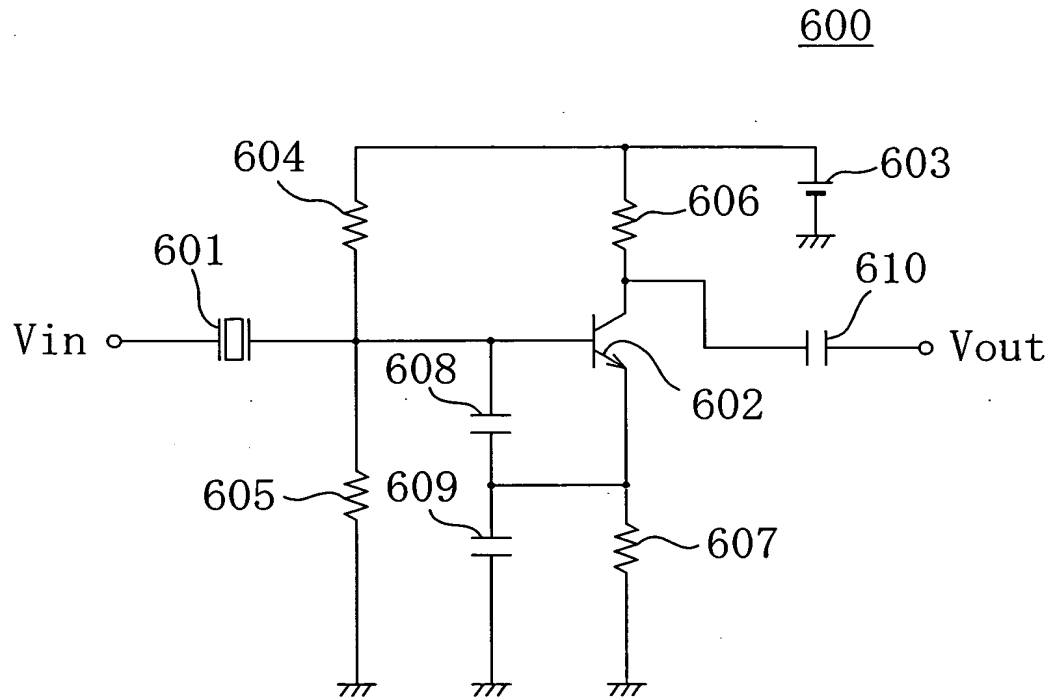


FIG. 14

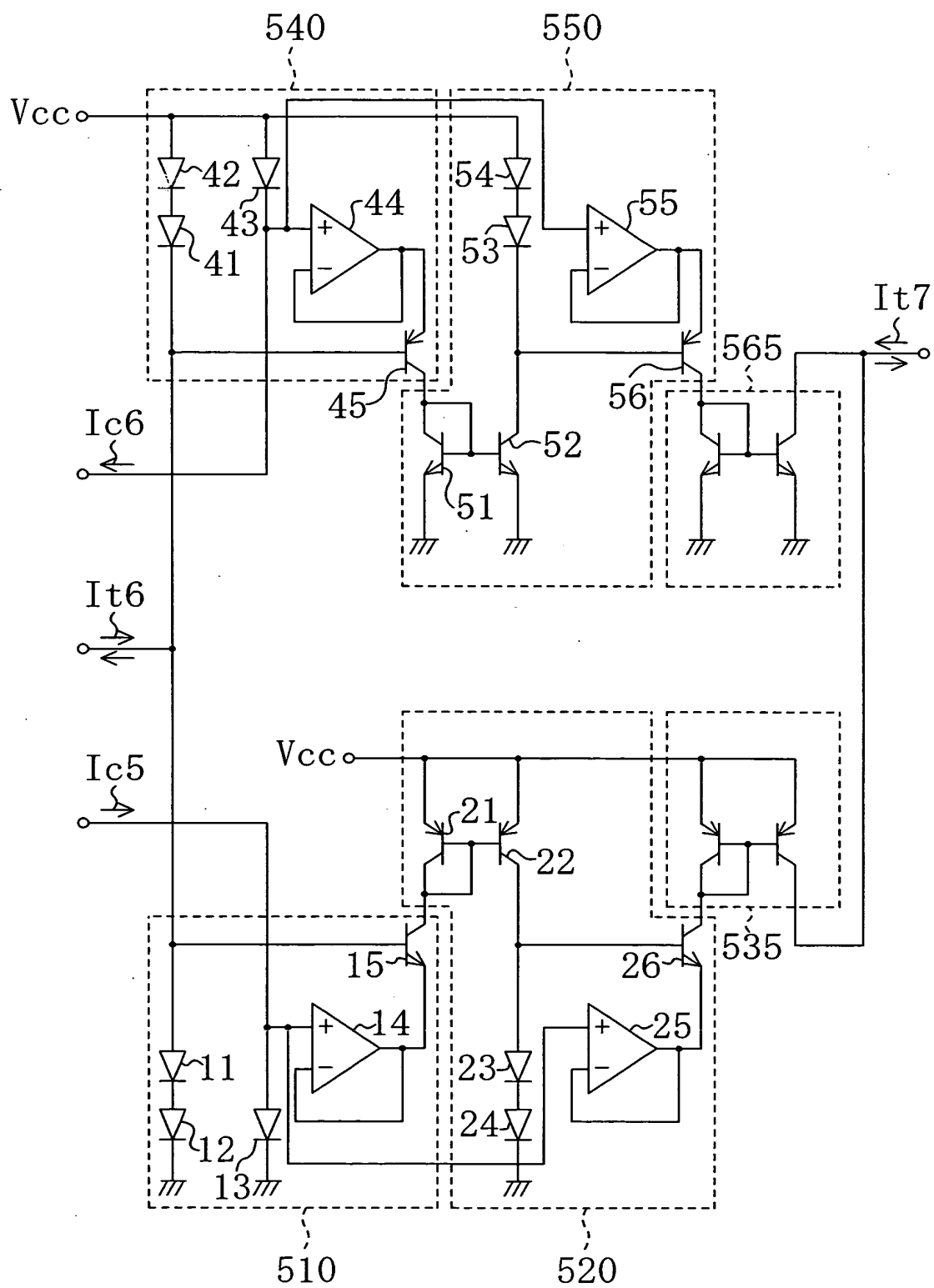


FIG. 15

